

Fig. 1

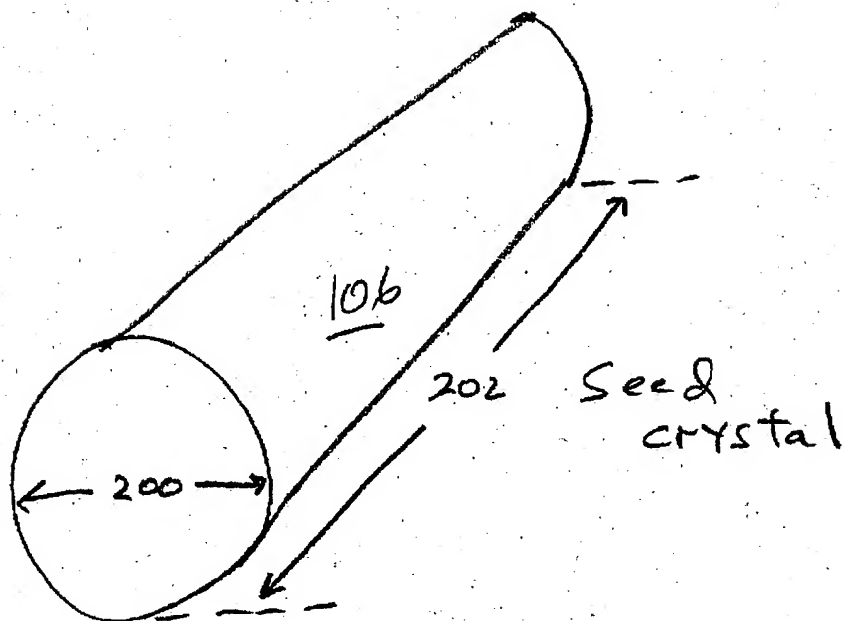


Fig. 2

100  
↙

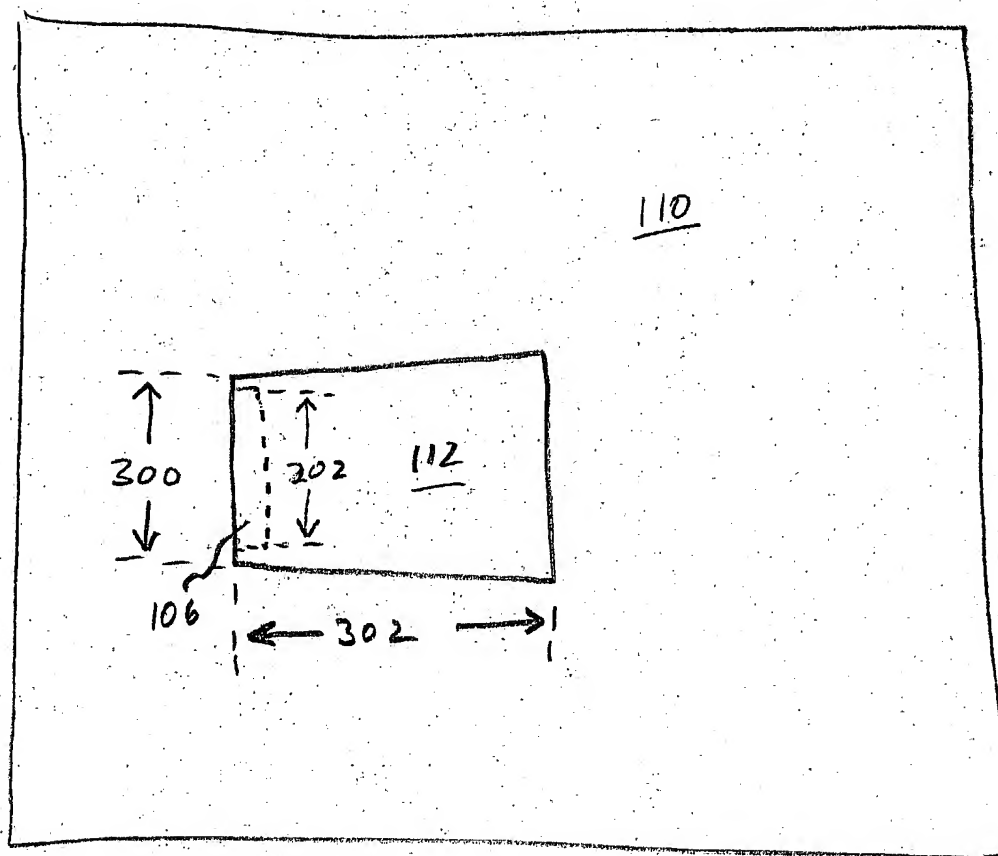


Fig. 3

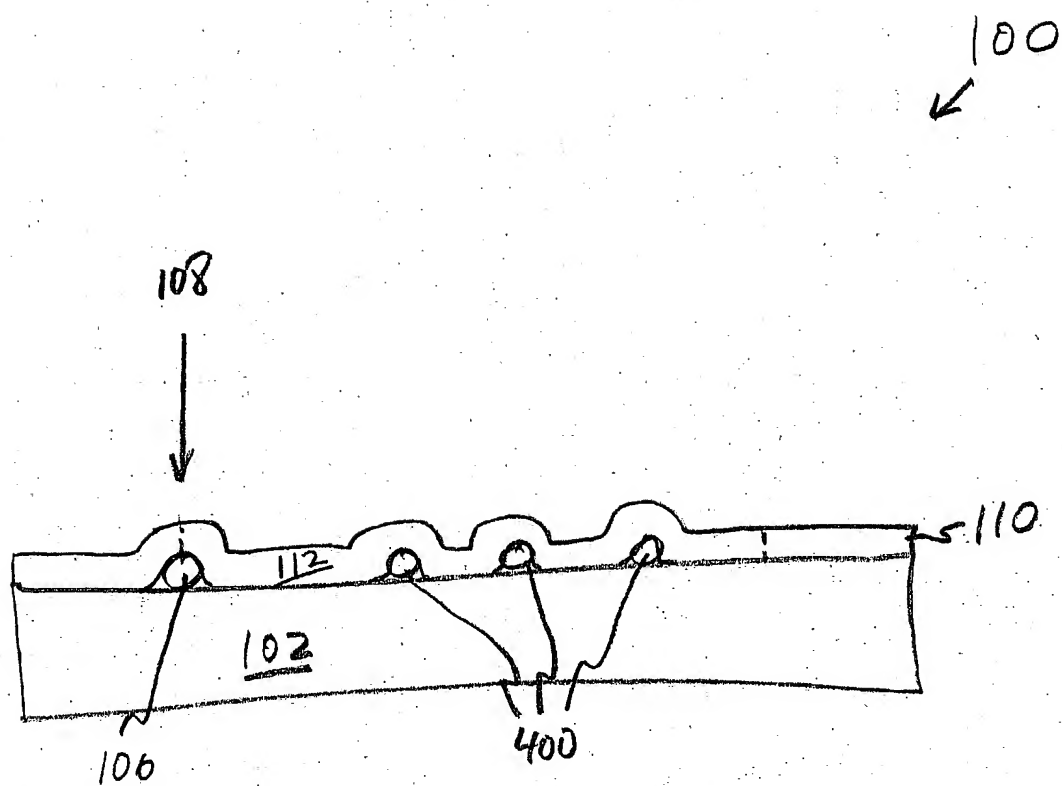


Fig. 4

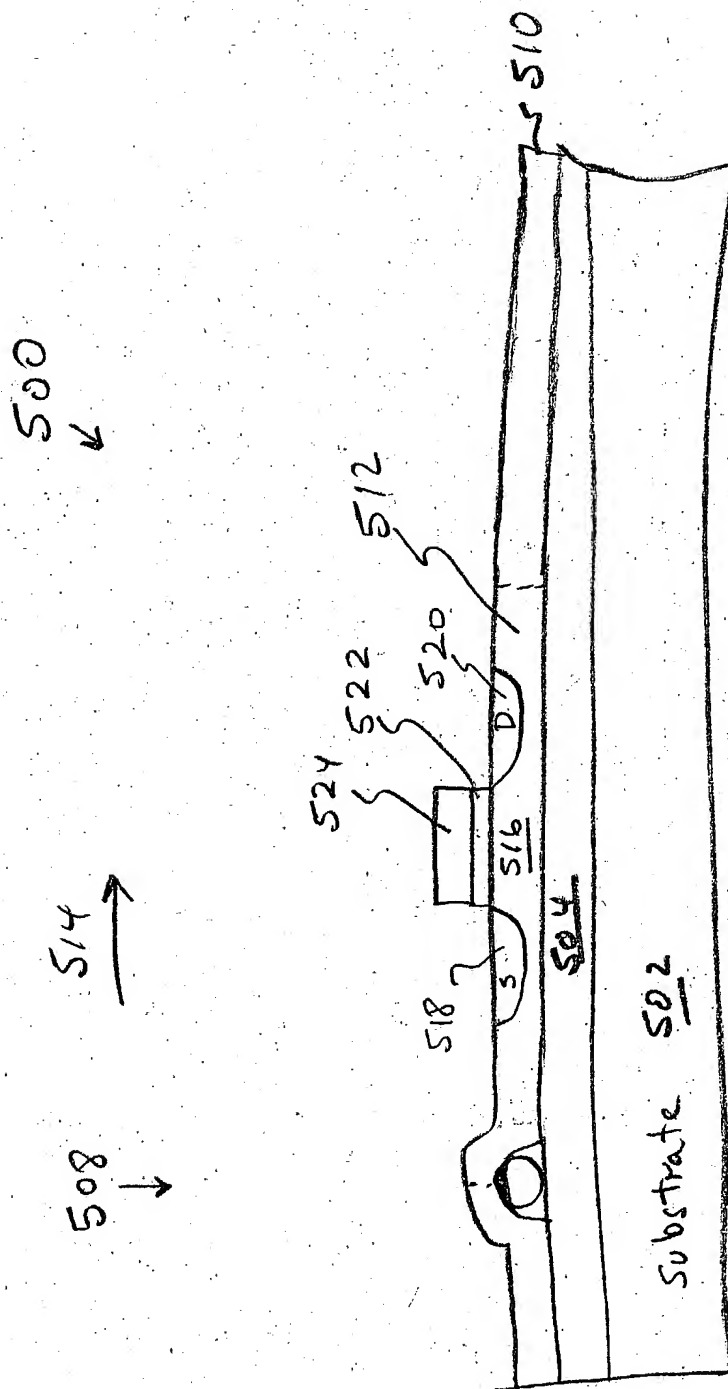


Fig. 5

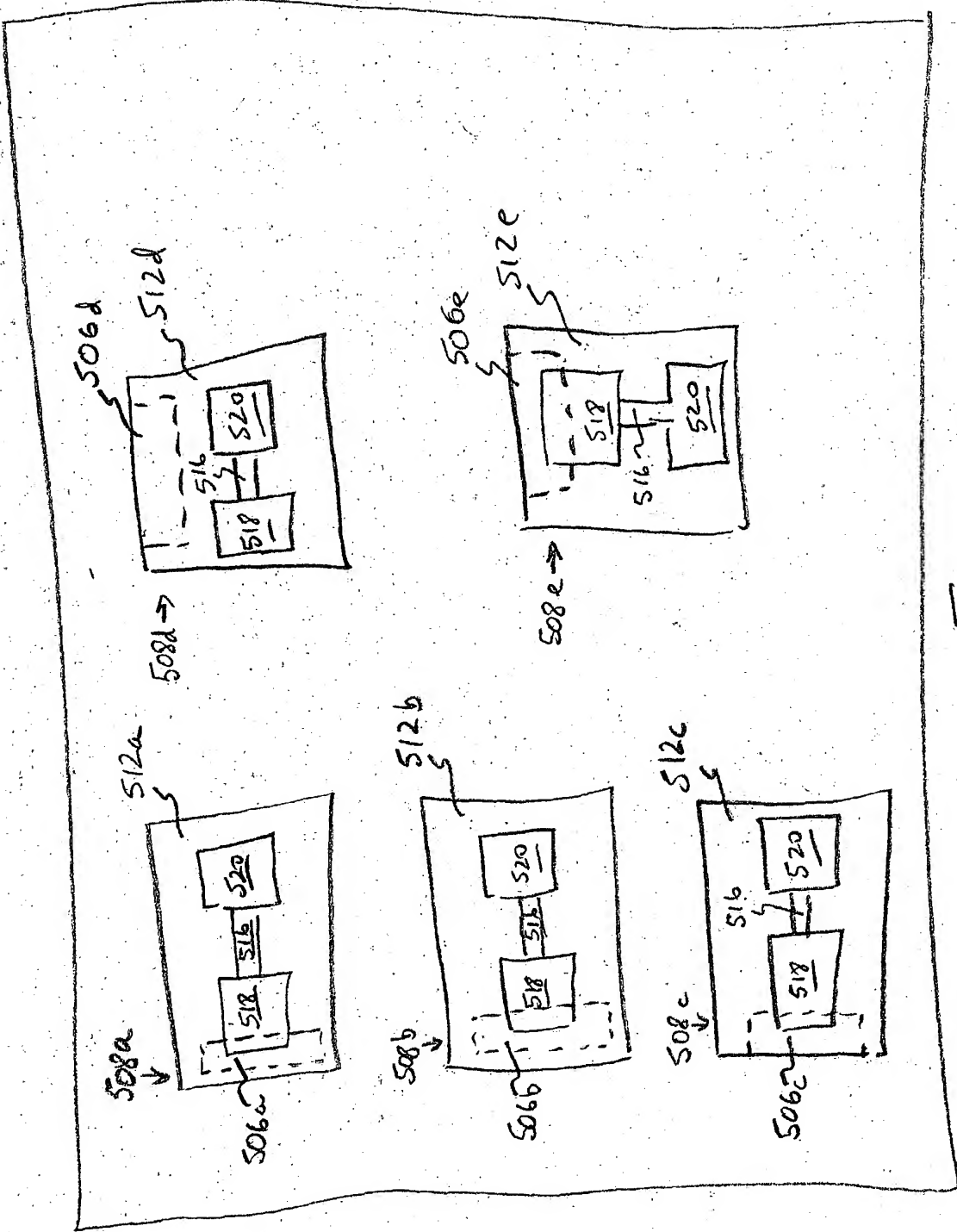


Fig. 6

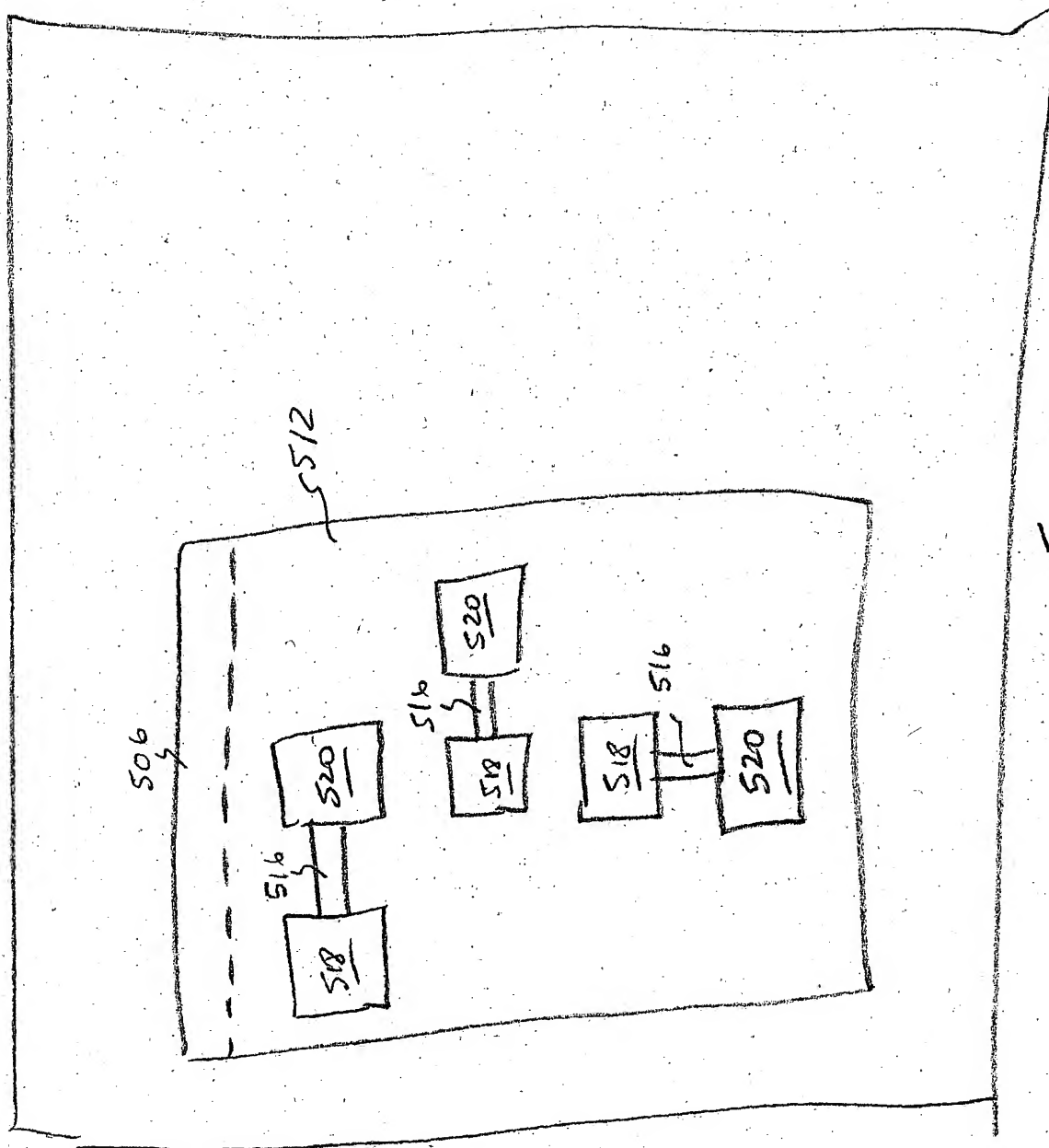
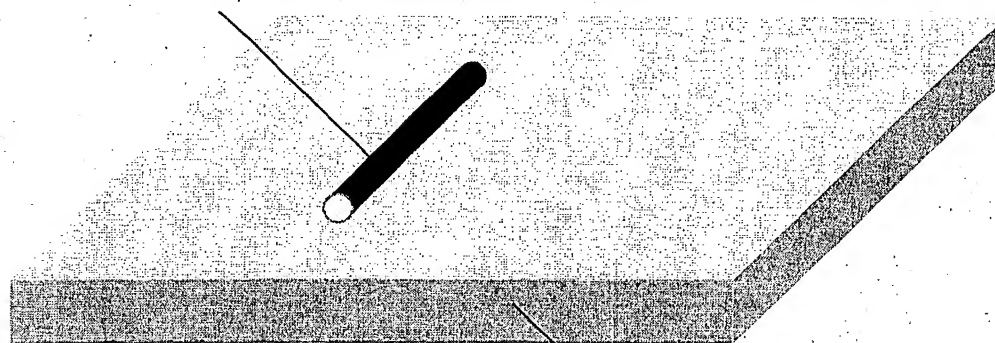


Fig. 7

*Semiconductor "seed" (i.e. Si nanowire: ~10nm dia, ~100um length)*



*Substrate (w/ or w/o additional coatings)*

Fig. 8



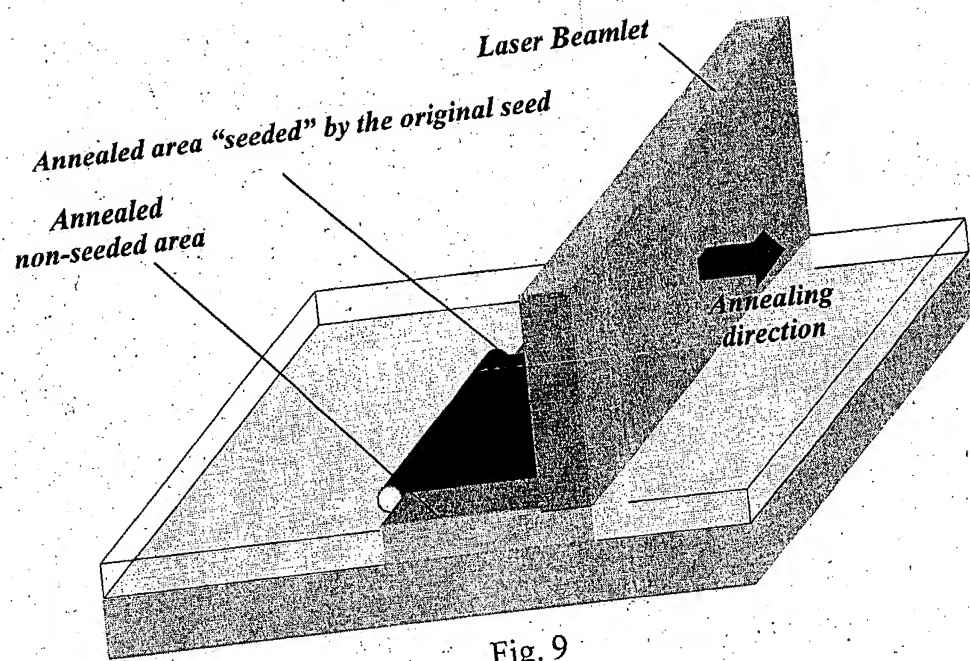


Fig. 9

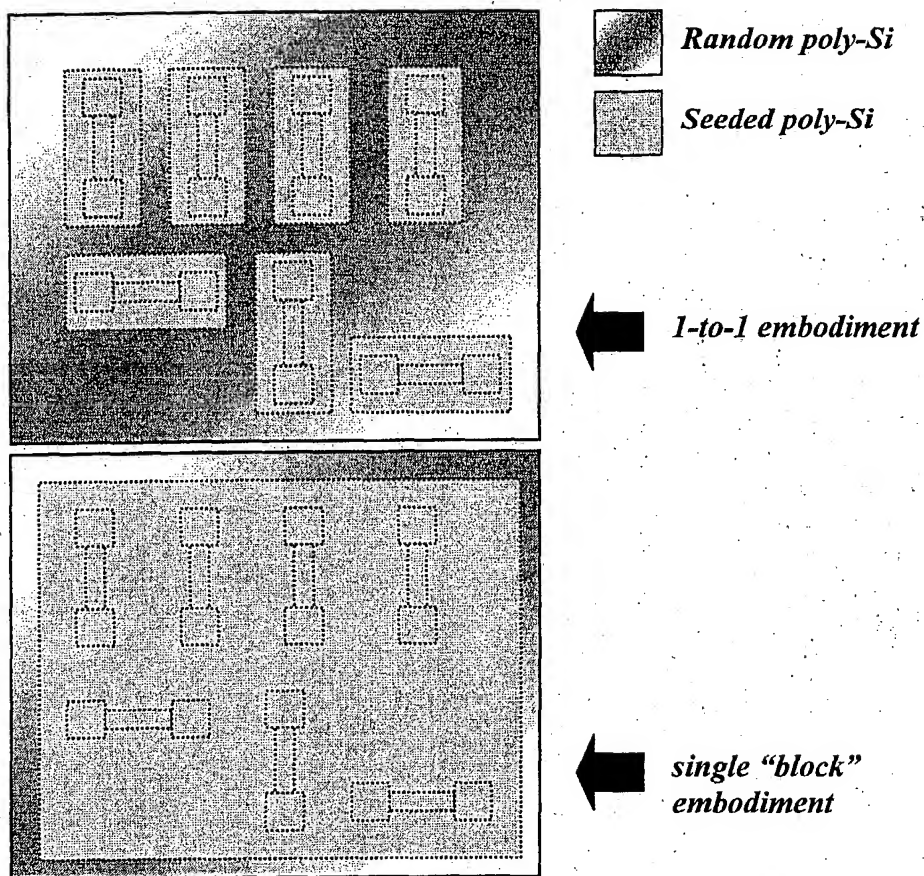


Fig. 10

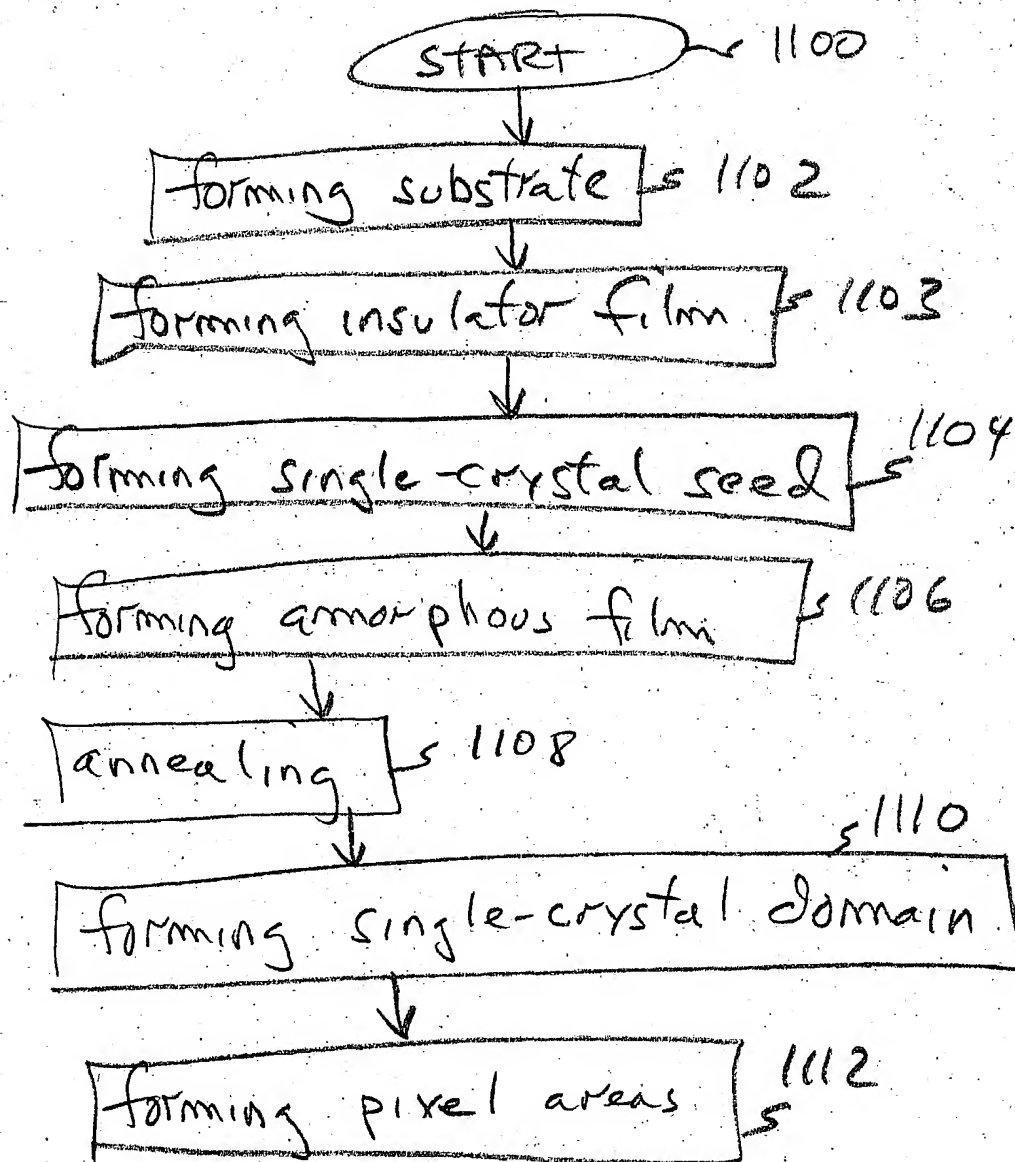


Fig. 11